

Fig. 1

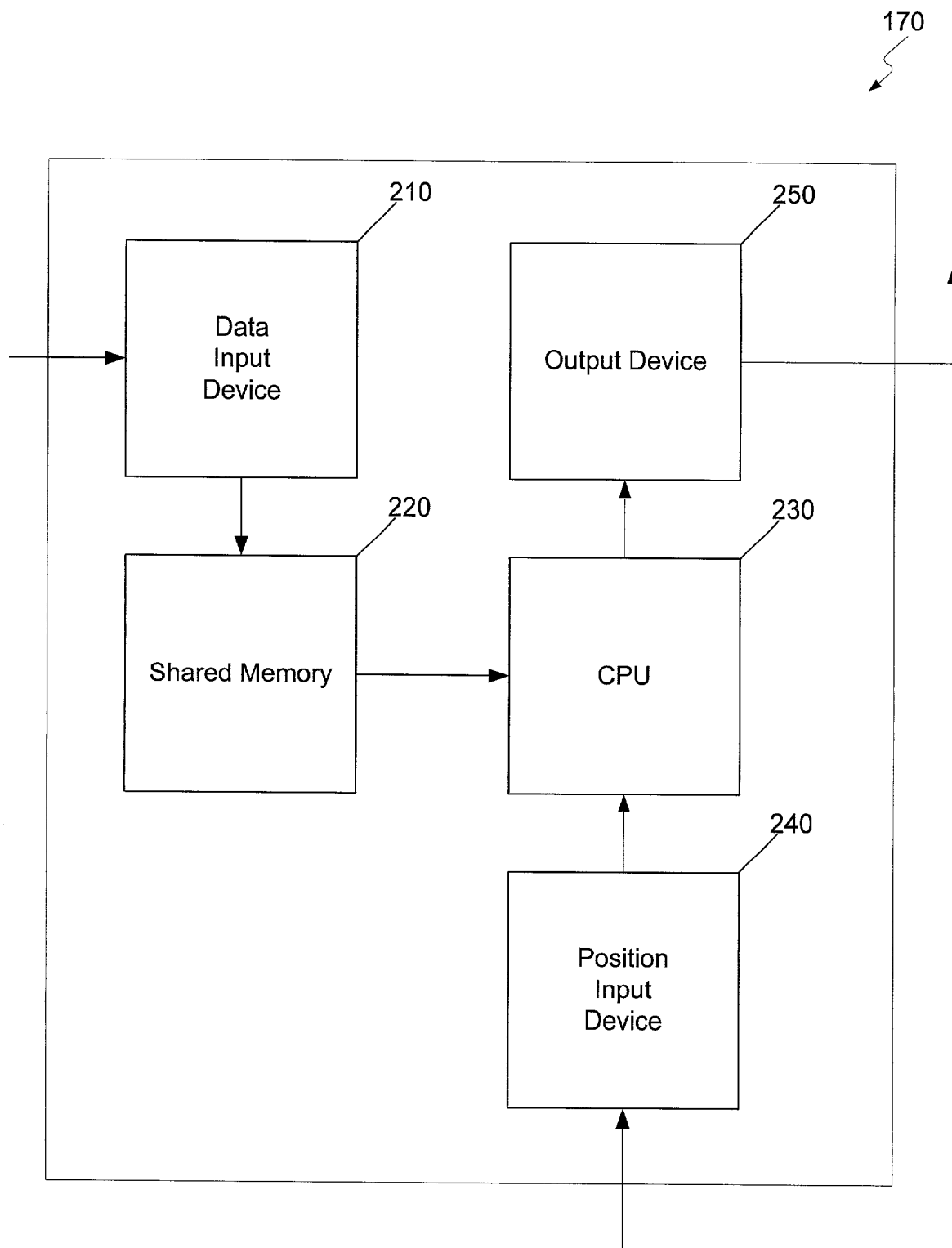


Fig. 2

FIG. 4 is a block diagram of a system 170. The system 170 includes an input device 210, a shared memory 220, a partial covariance processor 225, a CPU 230, an output device 250, and another input device 240. The input device 210 is connected to the shared memory 220 and the partial covariance processor 225. The shared memory 220 is connected to the partial covariance processor 225 and the CPU 230. The partial covariance processor 225 is connected to the CPU 230 and the output device 250. The CPU 230 is connected to the output device 250 and the input device 240. The input device 240 is connected to the CPU 230.

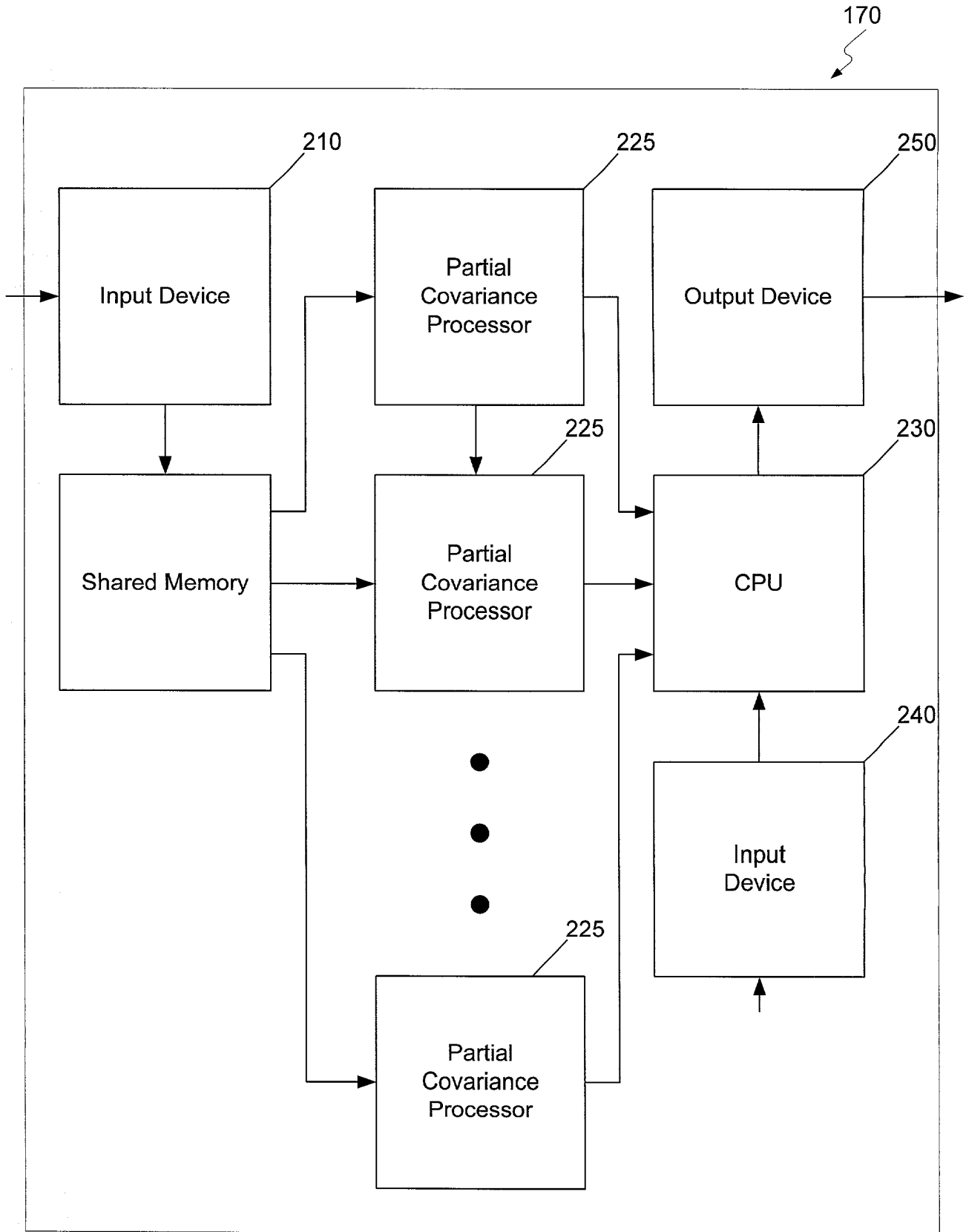


Fig. 4

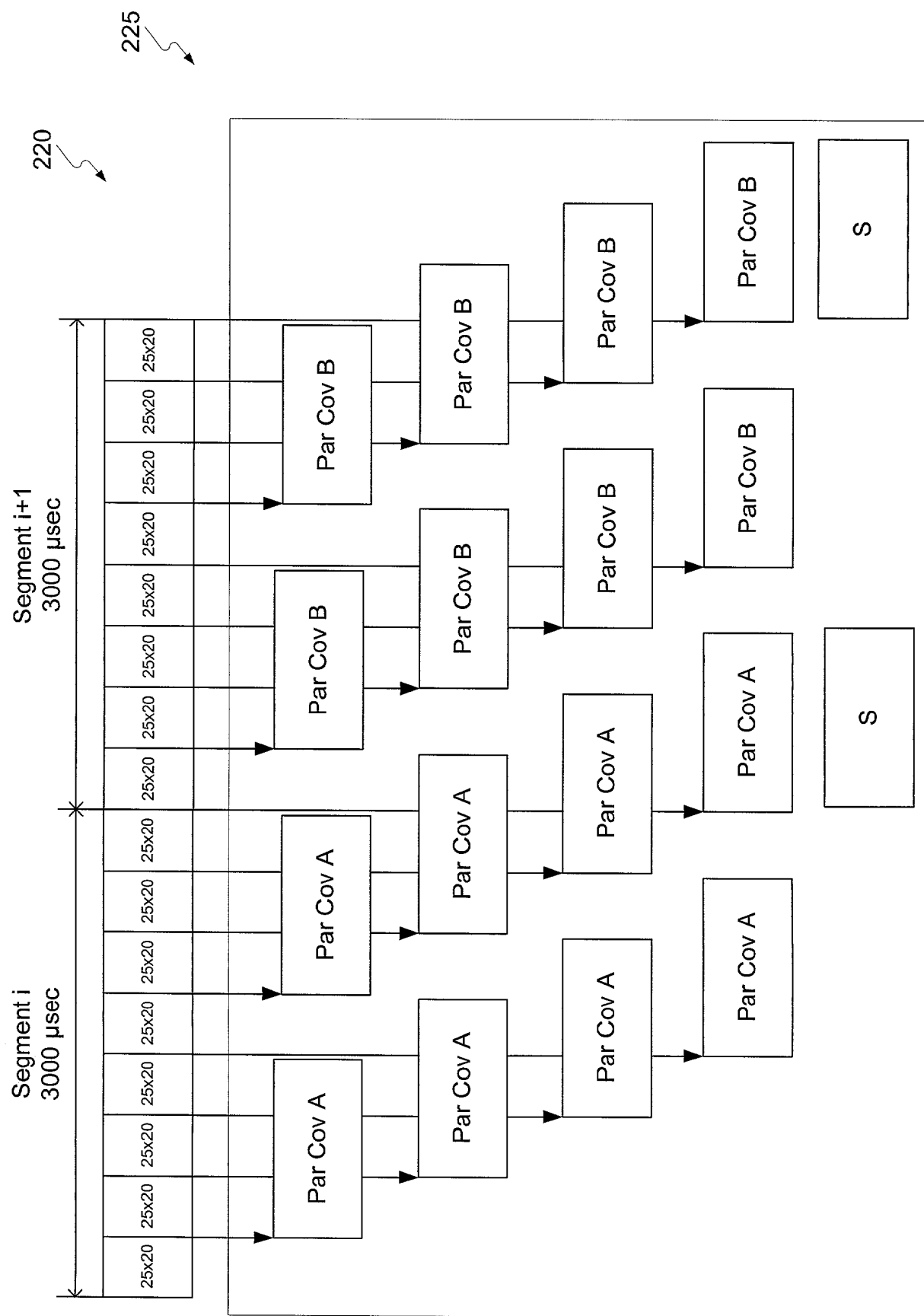


Fig. 5